

STPS1545

Power Schottky rectifier

Main product characteristics

I _{F(AV)}	15 A
V _{RRM}	45 V
T _j (max)	175° C
V _F (max)	0.57 V

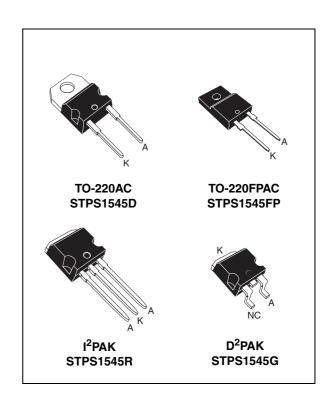
Features and Benefits

- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Insulated package: TO-220FPAC insulating voltage = 2000V DC capacitance = 12 pF
- Avalanche capability specified

Description

Single chip Schottky rectifier suited for Switch Mode Power Supply and high frequency DC to DC converters.

Packaged in TO-220AC, TO-220FPAC, I²PAK or D²PAK, this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection applications.



STPS1545 **Characteristics**

Characteristics 1

Table 1. **Absolute Ratings (limiting values)**

Symbol	Parame	Value	Unit		
V _{RRM}	Repetitive peak reverse voltage			45	V
I _{F(RMS)}	RMS forward voltage			30	Α
I _{F(AV)}	Average forward current $\delta = 0.5$	TO-220AC I ² PAK, D ² PAK	T _c = 155° C	15	Α
(, , ,	0 = 0.5	TO-220FPAC	T _C = 130° C		
I _{FSM}	Surge non repetitive forward current	t _p = 10 ms Sinusoidal	220	А	
I _{RRM}	Peak repetitive reverse current	$t_p = 2 \mu s \text{ square}$ F = 1 kHz	1	Α	
I _{RSM}	Non repetitive peak reverse current t _p = 100 µs square			3	Α
P _{ARM}	Repetitive peak avalanche power $t_p = 1 \mu s T_j = 25^{\circ}C$			6000	W
T _{stg}	Storage temperature range			-65 to + 175	°C
T _j	Maximum operating junction temperature (1)			175	°C
dV/dt	Critical rate of rise of reverse voltage			10000	V/µs

^{1.} $\frac{dPtot}{dTj} < \frac{1}{Rth(j-a)}$ thermal runaway condition for a diode on its own heatsink

Table 2. Thermal resistances

Symbol	Parameter		Value	Unit
R _{th(i-c)}	Junction to case	TO-220AC, I ² PAK, D ² PAK	1.6	°C/W
		TO-220FPAC	4.0	

Table 3. Static electrical characteristics (per diode)

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
I _B ⁽¹⁾	Reverse leakage current	T _j = 25°C	V - V			200	μΑ
'R`´	Reverse leakage current	T _j = 125°C	$V_R = V_{RRM}$		11	40	mA
		T _j = 125°C	I _F = 15 A		0.5	0.57	
V _F ⁽¹⁾ Forward voltage	Forward voltage drop	T _j = 25°C	I _F = 30 A			0.84	V
		T _j = 125°C	I _F = 30 A		0.65	0.72	

^{1.} Pulse test: $tp = 380 \mu s$, $\delta < 2\%$

To evaluate the conduction losses use the following equation: P = 0.42 x $I_{F(AV)}$ + 0.01 $I_{F}^{2}_{(RMS)}$

$$P = 0.42 \times I_{E(\Delta V)} + 0.01 I_{E^2(BMS)}$$

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Figure 1. Average forward power dissipation Figure 2. Average forward current versus versus average forward current ambient temperature (δ = 0.5)

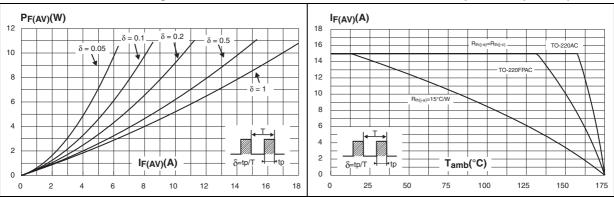


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

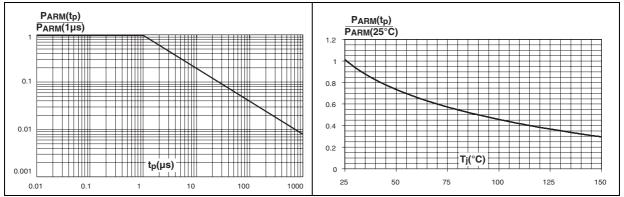
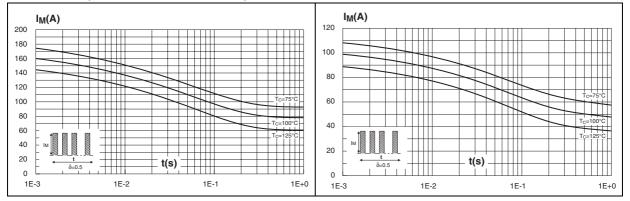


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values)
(TO-220AC, I²PAK D²PAK)

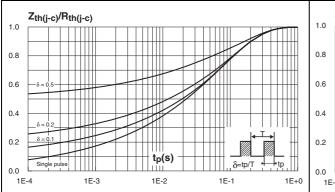
Figure 6. Non repetitive surge peak forward current versus overload duration (maximum values) (TO-220FPAC)



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Figure 7. Relative variation of thermal impedance junction to case versus pulse duration (TO-220AC, I²PAK D²PAK)

Figure 8. Relative variation of thermal impedance junction to case versus pulse duration (TO-220FPAC)



Z_{th(j-c)}/R_{th(j-c)}

0.8

0.6

0.6

0.4

0.2

0.2

0.0

1E-3

1E-2

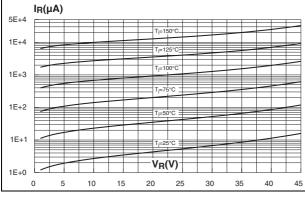
1E-1

1E+0

1E+1

Figure 9. Reverse leakage current versus reverse voltage applied (typical values)

Figure 10. Junction capacitance versus reverse voltage applied (typical values)



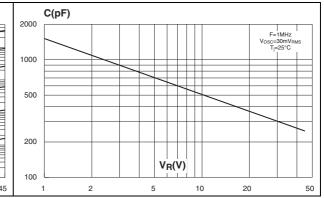
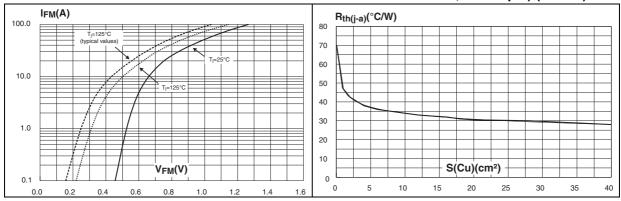


Figure 11. Forward voltage drop versus forward current (maximum values)

Figure 12. Thermal resistance junction to ambient versus copper surface under tab (Epoxy printed circuit board FR4, Cu=35 µm) (D²PAK)



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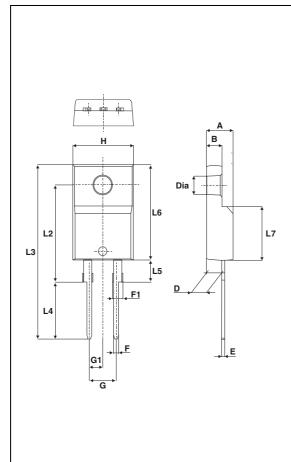
2 Package Information

Epoxy meets UL94, V0

Cooling method: by conduction (C)Recommended torque value: 0.55 Nm

• Maximum torque value: 0.7 Nm

Table 4. TO-220FPAC dimensions

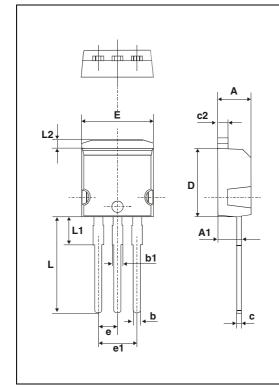


	Dimensions			
Ref	Millimeters		Inc	hes
	Min.	Max.	Min.	Max.
Α	4.4	4.6	0.173	0.181
В	2.5	2.7	0.098	0.106
D	2.5	2.75	0.098	0.108
Е	0.45	0.70	0.018	0.027
F	0.75	1	0.030	0.039
F1	1.15	1.70	0.045	0.067
F2	1.15	1.70	0.045	0.067
G	4.95	5.20	0.195	0.205
G1	2.4	2.7	0.094	0.106
Η	10	10.4	0.393	0.409
L2	16	Тур.	0.63	Тур.
L3	28.6	30.6	1.126	1.205
L4	9.8	10.6	0.386	0.417
L5	2.9	3.6	0.114	0.142
L6	15.9	16.4	0.626	0.646
L7	9.00	9.30	0.354	0.366
Dia.	3.00	3.20	0.118	0.126

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Table 5. I²PAK dimensions



	Dimensions				
Ref	Millimeters		Inches		
	Min.	Max.	Min.	Max.	
Α	4.40	4.60	0.173	0.181	
A1	2.49	2.69	0.098	0.106	
b	0.70	0.93	0.028	0.037	
b1	1.14	1.17	0.044	0.046	
b2	1.14	1.17	0.044	0.046	
С	0.45	0.60	0.018	0.024	
c2	1.23	1.36	0.048	0.054	
D	8.95	9.35	0.352	0.368	
е	2.40	2.70	0.094	0.106	
Е	10.0	10.4	0.394	0.409	
L	13.1	13.6	0.516	0.535	
L1	3.48	3.78	0.137	0.149	
L2	1.27	1.40	0.050	0.055	

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Table 6. D²PAK Package dimensions

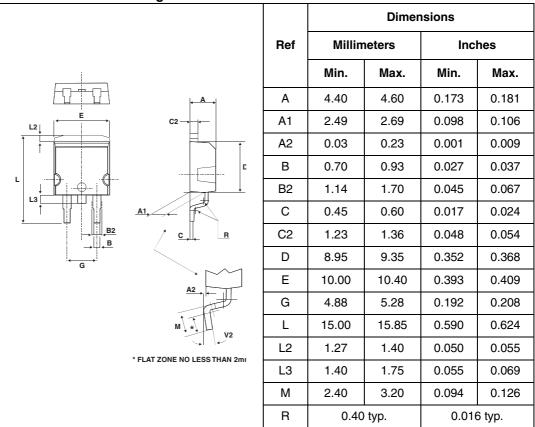
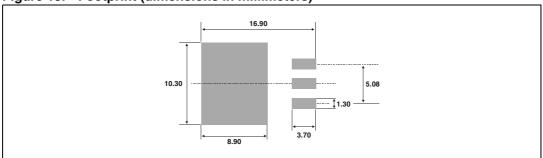


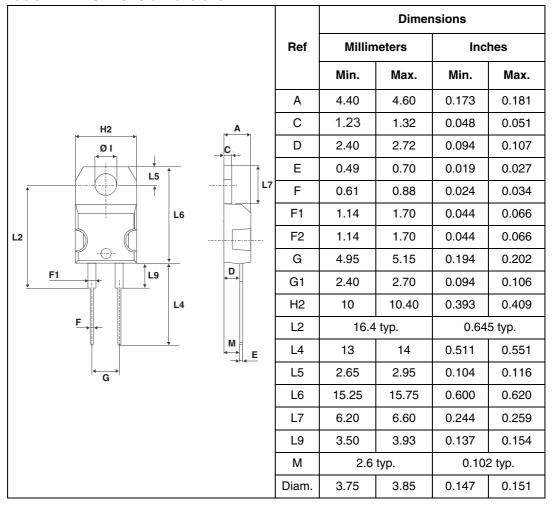
Figure 13. Footprint (dimensions in millimeters)



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Table 7. TO-220AC dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

3 Ordering Information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STPS1545D	STPS1545D	TO-220AC	1.86 g	50	Tube
STPS1545FP	STPS1545FP	TO-220FPAC	1.9 g	50	Tube
STPS1545R	STPS1545R	I ² PAK	1.7 g	50	Tube
STPS1545G	STPS1545G	D ² PAK	1.48 g	50	Tube
STPS1545G-TR	STPS1545G	D ² PAK	1.48 g	1000	Tape & Reel

4 Revision history

Date	Revision	Description of Changes
Jul-2003	5F	Last release.
21-Mar-2007	6	Removed ISOWATT package.

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